



# राष्ट्रीय प्रौद्योगिकी संस्थान हमीरपुर

हमीरपुर (हि.प्र.) - 177 005 (भारत)

[भारत सरकार शिक्षा मंत्रालय के तहत एक राष्ट्रीय महत्व का संस्थान]

**NATIONAL INSTITUTE OF TECHNOLOGY HAMIRPUR**

**HAMIRPUR (H.P.) - 177 005 (INDIA)**

[An Institute of National Importance under Ministry of Education (Shiksha Mantralaya)]

**(OFFICE OF THE DEAN (RESEARCH & CONSULTANCY))**

## Corrigendum

In reference to the advertisement published online on 10/06/2024 (NITH/HMR/R&C-2024/256-258) for the position of Junior Research Fellow (JRF) under the project titled "Design and Development of Cost-effective Alternate to Floating Metal Based RF-MEMS Shunt Capacitive Switch for B5G and 6G Applications," sponsored by SERB under its CRG Scheme, the following correction is made:

The emolument for the JRF position is revised from **Rs. 31000** to **Rs. 37000 plus HRA** as per institute/ SERB norms. This is as per the office memorandum of SERB: OM No. SB/S9/Z-06/2019 dated February 01, 2024, and consequently approval received in this matter from SERB.

All other terms and conditions mentioned in the original advertisement remain unchanged.

The application deadline for this advertisement has been **extended to July 10, 2024**.

Dr. Mahesh Angira (PI)

Associate Professor,

Department of ECE

Email: mahesh\_angira@nith.ac.in

No. NIT/HMR/R&C/2024/274-76

Date: 24/06/24

*Gaurav Paddar*  
24/06/2024  
Assistant Registrar (R&C)

Copy forwarded to:

1. Faculty Incharge (Computer Centre) with a request to upload it on the Institute Website.
2. Head, DoECE for information
3. Dr. Mahesh Angira, Associate Professor, DoECE for information and wide publicity.





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## Advertisement for the Post of Junior Research Fellow (JRF) (Purely Contractual)

Applications are invited from highly motivated and eligible candidates for 01 (one) position of Junior Research Fellow (JRF) in the research project entitled "*Design and Development of Cost-effective Alternate to Floating Metal Based RF-MEMS Shunt Capacitive Switch for B5G and 6G Applications*" funded by (SERB) under its Core Research Grant Scheme in the Department of Electronics and Communication Engineering, NIT Hamirpur (HP) as per detail envisaged below.

| S.No. | Name of the Post       | Junior Research Fellow (One)   |
|-------|------------------------|--|
| 1     | Qualification          | Graduate (B.E, B.Tech)/Post Graduate (M.E, M.Tech) Degree in Electronics/ECE/VLSI Design/Microelectronics/MEMS/RF/Microwave and other relevant related field s selected through a process described through any one of the following:-<br><ul style="list-style-type: none"> <li>Scholars who are selected through National Eligibility Tests- CSIR- UGC NET including lectureship (Assistant Professorship) and GATE.</li> <li>The selection process through National level examinations conduct by Central Government Departments and their Agencies and Institutions such as DST,DBT,DAE,DRDO, MoE ,ICAR, ICMR, IIT, IISc, IISER, NISER etc.</li> </ul> |
| 2     | Emoluments             | Rs. 31,000/- per month plus HRA as per institute norms   |
| 3     | Nature of Appointments | The position is purely on contract basis with the initial appointment for a period of one year from the date of joining extendable up to the project duration subject to the satisfactory performance of the individual.   |
| 4     | Project Duration       | 03 years(Project Start Date: 17 <sup>th</sup> February,2024)   |
| 5     | Number of posts        | 01   |
| 6     | Age Limit              | Max. age limit for JRF is 35 years as on last date of receipt of applications. Upper age limit will be relaxed up to 5 years for reserved category candidates (SC/ST/Persons with Disability (PWD) and 3 years for OBC-Non-creamy layer candidates.  |

### How to Apply:

- Interested candidates may send their duly filled Application from ( Annexure-1) along with brief resume/curriculum vitae and self attested copies of the relevant certificates/documents to the Principal Investigator (PI) of the Project, Dr. Mahesh Angira, Associate professor, Department of Electronics and Communication Engineering; NIT Hamirpur (HP)-177005
- The candidates must also submit soft copies of all their relevant documents through an email addressed to: mahesh\_angira@nith.ac.in

The list of shortlisted candidates will be displayed on the Institute's website. No separate letter will be issued to the shortlisted candidates for appearing in further screening and selection process. No TA/DA will be paid for attending the interview. **The last date of receiving applications is 26<sup>th</sup> June, 2024.** The date of interview along with the shortlisted candidates for interview will be displayed on the Institute's website or informed through email by the concerned PI. NIT Hamirpur reserves the right to set higher criteria for shortlisting eligible candidates from those satisfying the advertised qualification and requirements of the project post.

**Note:** -The selected candidate shall be encouraged to apply for the PhD Programme of the Institute.

Dr. Mahesh Angira,  
Project PI and Associate Professor,  
Department of Electronics & Communication Engineering,  
NIT Hamirpur, H.P., India-177005.  
Email\_mahesh\_angira@nith.ac.in

Assistant Registrar (R & C)  
NIT Hamirpur (HP)

No. NITH/HMR/R&C-2024/256-258

Dated: 10/06/24

### Copy to the:

- Faculty Incharge (Computer Centre) with the request to upload the Institute Websites.
- Head, DoECE for information.
- Dr. Mahesh Angira, Associate Professor-cum PI, DoECE for information & wide publicity



Annexure-1

APPLICATION FORM FOR THE POSITION OF JRF UNDER SERB CRG SPONSORED  
RESEARCH PROJECT

1. Name of the candidate:
2. Father's Name:
3. Date of Birth (DD/MM/YYYY):
4. Sex (Male/Female):
5. Marital Status (Single/Married):
6. Category (Open/OBC/SC/ST/PwD):
7. Address for Correspondence:
8. Educational Qualification:

Paste your  
Photo Here

| Degree           | Specialization<br>/Subjects | Board/University | Year of<br>Passing | Division | Percentage/<br>CGPA |
|------------------|-----------------------------|------------------|--------------------|----------|---------------------|
| 10 <sup>th</sup> |                             |                  |                    |          |                     |
| 12 <sup>th</sup> |                             |                  |                    |          |                     |
| B.Tech./B. E     |                             |                  |                    |          |                     |
| M.Tech./M.E.     |                             |                  |                    |          |                     |
| Any other        |                             |                  |                    |          |                     |

9. GATE/NET/ICMR/DBT- score earned/qualified:


| Branch | Year Qualified | Score and Rank | Valid up to |
|--------|----------------|----------------|-------------|
|        |                |                |             |
|        |                |                |             |

10. Research/ Industrial/ Teaching experience (if any)

PTO

Q





| S.No. | Organization/Industry | Date of Entry | Date of Leaving | Total experience |
|-------|-----------------------|---------------|-----------------|------------------|
|       |                       |               |                 |                  |

11. Publications/ Conference participation:

12. Any other relevant information:

13. Declaration

I \_\_\_\_\_ hereby declare that all the statements made in this application are true, complete, and correct to the best of my knowledge and belief. In the event of information found false or incorrect or any ineligibility being detected before or after selection, my candidature is liable to be cancelled. I confirm that I am ready to attend the interview either in online or offline mode.

Date:

Signature of Candidate