



Research Facility Training Programme on VLSI Design and IC Packaging Techniques



(Under Scientific Social Responsibility (SSR) Policy of SERB)

21 December 2022

Funded by: Core Research Grant Scheme of Science and Engineering Research Board, Department of Science and Technology, Government of India

Organized by: Department of Electronics and Communication Engineering, National Institute of Technology, Hamirpur (HP)

About NIT Hamirpur

National Institute of Technology (NIT) Hamirpur is one of the thirty-one NITs of the country, established in 1986 as Regional Engineering College, as a joint and cooperative enterprise of the Govt. of India and Govt. of Himachal Pradesh. The goals of the Institute as embodied in the logo are truly remarkable in their scope of vision. The Institute provides Undergraduate, Postgraduate and Doctorate Degree Programmes in Engineering, Sciences & Humanities; fostering the spirit of National Integration among the students, a close interaction with industry and a strong emphasis on research, both basic and applied. NIT Hamirpur has secured 9th Rank in the Indian Institutional Ranking Framework (IIRF) 2022. The recent placement statistics of the Institute are also excellent with many students getting more than one crore annual package in leading multi-national companies.

About Training Programme

Three-dimensional integrated circuits (3-D ICs) have aroused intense research interest for the technology realm of 'More-than-Moore' in the past decades. These high-aspect-ratio vertical interconnects also known as through package vias (TPVs) can mitigate the limitations of conventional planar ICs and enable the implementation of heterogeneous 3-D integrated circuits and systems. TPVs provide an exciting alternative for the realization of high-performance electrical systems with higher speed, high-density packaging, wider bandwidth, and reduced power consumption.

This one-day workshop would acquaint the researchers about latest innovations in the field of IC packaging and research and development in VLSI Design. The workshop is also aimed to provide hands-on experience to the PG and PhD students in handling softwares and skill development on themes required for the conduct of research work. The programme is also meant to support motivated PG and PhD level students, who are having a strong willingness to get excellence in their scientific and engineering research pursuits.

Eligibility Criteria to Attend

The training programme is open to PG and PhD research scholars of only NIT Hamirpur. Selection shall be on first come first serve basis, with preference to students whose research interests align with the theme of the training programme and allied fields. **Total number of seats is limited to 10.** The applicants have to submit the registration form in the attached format duly signed by the Head of concerned Department. **There is no registration fees.**

Patron

Prof. H. M. Suryawanshi, Director, NIT Hamirpur

Chairperson

Dr. Gargi Khanna, Head, Department of ECE, NIT Hamirpur

Coordinator-cum-Treasurer

Dr. Rohit Dhiman, Assistant Professor, Department of ECE

Convener

Dr. Rajeevan Chandel, Professor, Department of ECE

Address for Correspondence: Dr. Rohit Dhiman, Coordinator, Department of Electronics and Communication Engineering, NIT Hamirpur (HP). E-mail: rohithdhan@nith.ac.in

Important Dates

Registration opens

June 1, 2022

Last date for application

June 15, 2022

Display of shortlisted candidates

June 20, 2022

Programme date

December 21, 2022

Venue: Seminar Hall, Department of Electronics and Communication Engineering, NIT Hamirpur (HP) 177 005, INDIA