

Annexure 1

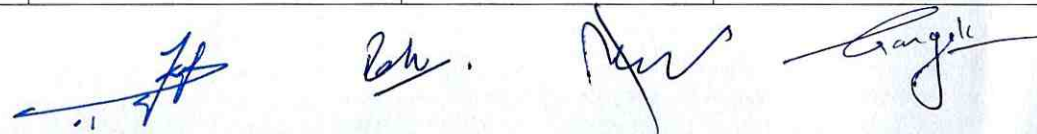
(List of Eligible/ Shortlisted Candidates)

(VLSI DESIGN, COMMUNICATION SYSTEM AND NETWORKS)

S. No.	Name of Candidate	Father's Name	Category	Specialization
1	Sahil Koundal	Sampath Kumar	SC	VLSI DESIGN
2	Aakarshit Garg	Virender Sharma	General	VLSI DESIGN
3	Pankaj Sharma	Subhash Chand	General	VLSI DESIGN
4	Sitakshi Rankta	Laiq Ram	SC	VLSI DESIGN
5	Indradeep Bhattacharyay	Subrata Bhattacharyay	General	VLSI DESIGN
6	Sourav Verma	Sh. Yash Paul	SC	VLSI DESIGN
7	Rohit Kumar	Sh. Ram Lal Near	ST	Communication System and Networks
8	Rohit Kumar	Sh. Ram Lal Near	ST	VLSI Design
9	Shekhar Chauhan	Vijay Kumar	GENERAL	Communication System and Networks
10	Arundhati Jasta	Sh. Ajay Kumar Jaasta	GENERAL	VLSI DESIGN

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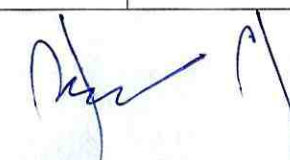

11	Vinay Kumar	Sh. Rajinder Parshad	GENERAL	Communication System and Networks
12	Sahil Koundal	Sampath Kumar	SC	Communication System and Networks
13	Kshitiz Shukla	Sh. Pradip Kumar Shukla	GENERAL	VLSI DESIGN, Communication System and Networks
14	Prathibha Kumari	Sh. Ankesh Kapil	GENERAL	VLSI DESIGN, Communication System and Networks
15	Sandeep Thakur	Sh. Sher Singh	GENERAL	VLSI DESIGN
16	Lakshy Sood	Sh. Ghanshyam sood	GENERAL-EWS	VLSI DESIGN, Communication System and Networks
17	Varun Rana	Vinod Kumar Rana	GENERAL	VLSI DESIGN, Communication System and Networks
18	Naman Rana	Randhir Singh Rana	GENERAL	Communication System and Networks, VLSI DESIGN
19	Sakshi Sharma	Sh. Yoginder singh	GENERAL	VLSI DESIGN, Communication System and Networks
20	Vidushi Sharma	Sh. Rajeev Kumar	GENERAL	Communication System and Networks, VLSI DESIGN
21	Happy Rana	Sh. Rilak Raj	GENERAL	VLSI DESIGN, Communication System and Networks
22	Sumit Rana	Sh. Balwant Singh Rana	GENERAL	VLSI DESIGN, Communication System and Networks
23	Simran Dogra	Sh. Vikran Jeet Dogra	OBC-NCL	VLSI DESIGN, Communication System and Networks
24	Bharat	Sh. Desh Raj	GENERAL	VLSI DESIGN, Communication System and Networks
25	Deepak Kumar	Sh. Gopal Pandit	OBC-NCL	VLSI DESIGN





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26	Vinayak Kumar Tripathi	Sh. Vinod Kumar Tripathi	GENERAL-EWS	VLSI DESIGN, Communication System and Networks
27	Ankush Gathania	Sh. Kesar Singh	GENERAL	Communication System and Networks, VLSI DESIGN
28	Ankita Jamwal	Sh. Ramesh Chand Jamwal	GENERAL	VLSI DESIGN
29	Kumara Abhinandan Patial	Sh. Ram Kumar	GENERAL	VLSI DESIGN, Communication System and Networks
30	Vardan Kumar	Sh. Kawal Kumar	GENERAL	VLSI DESIGN, Communication System and Networks
31	Mohit Rattan	Sh. Jai Pal Singh	ST	VLSI DESIGN, Communication System and Networks
32	Rajat Kumar	Sh. Manohar Lal	SC	VLSI DESIGN, Communication System and Networks
33	Ajay Kumar	Kushwaha	OBC-NCL	VLSI DESIGN, Communication System and Networks
34	Aryan	Sh. Nitin Kumar	SC	VLSI DESIGN, Communication System and Networks
35	Vanshita Mahajan	Sh. Vinay Mahajan	GENERAL	Communication System and Networks, VLSI DESIGN
36	Aakash Singh Thakur	Sh. Vijay Kumar	GENERAL	Communication System and Networks, VLSI DESIGN
37	Rohan Paswan	Sh. Lalit Paswan	SC	VLSI DESIGN, Communication System and Networks
38	Diksha	Sh. Bikram Singh	GENERAL	Communication System and Networks, VLSI DESIGN
39	Akshay Kumar	Sh. Rakesh Kumar	SC	VLSI DESIGN, Communication System and Networks


40	Aanchal Sharma	Sh. Jagdish Chand Sharma	GENERAL	VLSI DESIGN, Communication System and Networks
41	Kriti	Sh. Hem Raj	GENERAL	Communication System and Networks, VLSI DESIGN
42	Manik Maasand	Sh. Umesh Masand	OBC-NCL	VLSI DESIGN
43	Mir Owais Towheed	Abdul Ahad Mir	OBC-NCL	VLSI DESIGN, Communication System and Networks
44	Abhay Kumar	Sh. Dilip Kumar	OBC-NCL	VLSI DESIGN, Communication System and Networks

(List of Non-Eligible Candidates)

S.No.	Name of Candidate	Father's Name	Category	Reason for Non-Eligibility
1.	Ritish Ranjan	Sh. Ashwani Kumar	GENERAL	CGPI in B.Tech is 6.1


 Dr. K. S Pandey
 (Member, MSSC)

(on-leave)
 Dr. M. R Bharti
 (Member, MSSC)


 Dr. Pardeep Singh
 (Member, MSSC)


 Dr. Rohit Dhiman
 (Convener, MSSC)


 Dr. Gargi Khanna
 (Chairperson, MSSC)