



राष्ट्रीय प्रौद्योगिकी संस्थान हमीरपुर
हमीरपुर (हि.प्र.) - 177 005 (भारत)
[भारत सरकार शिक्षा मंत्रालय के तहत एक राष्ट्रीय महत्व का संस्थान]
NATIONAL INSTITUTE OF TECHNOLOGY HAMIRPUR
HAMIRPUR (H.P.) - 177 005 (INDIA)
[An Institute of National Importance under Ministry of Education (Shiksha Mantralaya)]

{OFFICE OF THE DEAN (RESEARCH & CONSULTANCY)}

Advertisement for Post of Senior Project Associate and Project associate (Purely contractual)

Applications are invited from highly motivated and eligible candidates for **One (01) position of Senior Project Associate** and **Two (02) position of Project Associate** in the project titled "ASIC and Package Design of Ultra Small Atomic Clock" funded by SMDP-C2S under Ministry of Electronics and Information Technology, Govt. of India, in the Department of Electronics and Communication Engineering, NIT Hamirpur as per details envisaged below:

Post	Emoluments	Qualification
Senior Project Associate (No. of Vacancy: 01)	Rs. 39,000/- PM (consolidated)	<ul style="list-style-type: none"> M.E. / M.Tech. Degree in relevant specialization of EC/EE/IN. The candidate must have obtained M.E./M.Tech through GATE/CSIR-UGC NET or any other National Exam Qualification conducted by Central Govt. departments or institutes such as DST, DBT,DAE,DOS,DRDO,MHRD, ICAR, ICMR, IIT, IISc, IISER etc. The candidate is required to familiar with Microelectronics, and experience of using Linux operating system and VLSI IC design tools such as Cadence, Synopsis. <p>Preference shall be given to candidate having Doctoral Degree in relevant discipline</p>
Project Associate (No. of Vacancy: 02)	Rs. 30,000/- PM (consolidated)	<ul style="list-style-type: none"> M.E. / M.Tech. Degree in relevant specialization of EC/EE/IN through GATE or B.Tech. (EC/EE/Instrumentation) with a GATE score in EC/EE/IN discipline The candidate must have obtained M.E./M.Tech through GATE/CSIR-UGC NET or any other National Exam Qualification conducted by Central Govt. departments or institutes such as DST, DBT,DAE,DOS,DRDO,MHRD, ICAR, ICMR, IIT, IISc, IISER etc. The candidate is required to familiar with Microelectronics, and experience of using Linux operating system and VLSI IC design tools such as Cadence, Synopsis.

1)	Name of the Project	ASIC and Package Design of Ultra Small Atomic Clock
2)	Name(s) of Project Investigator	Dr. K.S.Pandey (PI) Dr. Gargi Khanna (Co-PI)
3)	Nature of appointment	The position is purely on contract basis with the initial appointment for a period of one year from the date of joining, extendable upto the project duration subject to the satisfactory performance of the individual and provisions under the project.
4)	Project Duration	Five Years
5)	Age Limit	Maximum age limit for Senior Project Associate is 40 years and for Project Associate is 35 years, as on last date of receipt of applications. The relaxation in age limit and qualification will be applicable as per GoI Norms

How to apply:

- Interested candidates may send their duly filled Application form (Annexure-I) along with brief resume/curriculum vitae and self-attested copies of the relevant certificates/documents to the Co-Principal Investigator (Co-PI) of the project, Dr. Gargi Khanna, Associate Professor, Department of Electronics and Communication Engineering, NIT Hamirpur (HP) 177005.
- The candidates must also submit softcopies of all their relevant documents through an e-mail addressed to: gargi@nith.ac.in

NIT Hamirpur reserves the right to fix higher criteria for shortlisting of eligible candidates from those satisfying advertised qualifications and requirement of the project post. The list of shortlisted candidates will be displayed on the Institute's website. No separate letter will be issued to the shortlisted candidates for appearing in further screening and selection process. **No TA/DA will be paid for attending the interview. The last date of receiving applications is 30th January-2024.**

The date of interview along with the shortlisted candidates called for interview will be displayed on the Institute's website or informed through email.

Note: The selected candidate shall be encouraged to apply for PhD Programme of the Institute as per the prevailing norms.

Dr. Gargi Khanna (Co-PI)
Associate Professor, Department of ECE,
NIT Hamirpur (HP) 177 005, India


Assistant Registrar (R&C)

No. NITH/HMR/R&C-2024/1010-1012

Dated: 08/01/24

Copy forwarded to:

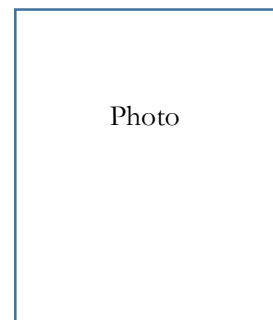
- ✓ 1. Faculty Incharge (Computer Centre) with the request to upload on the Institute Website.
2. Head, DoECE for information.
3. Dr. K.S.Pandey and Dr. Gargi Khanna, Department of ECE for information and wide publicity.

APPLICATION FOR SENIOR RESEARCH ASSOCIATE/RESEARCH
ASSOCIATE POSITION

Name of the Project: **ASIC and Package Design of Ultra Small Atomic Clock**

Applied For the Post of.....

1. Full Name:
2. Date of Birth:
3. Gender M/F:
4. Marital Status:
5. Nationality:
6. Category:
7. Physically handicapped: Y/N
8. Father's Name:
9. Address of Correspondence:



10. Mobile No.:

11. E-mail Address:

12. Academic Record:

- A. GATE/CSIR/UGC or any other National Exam Qualification conducted by Central Govt. departments as listed in the advertisement details with Percent marks/Rank, year of qualifying etc.

Name of the examination	Year of passing	Score

B. Qualifying Degree:

Examination (mention specialization if applicable)	Name of School/College/ University/Institute With city and state	Year of Passing	Specialization	% of Marks/CGPA obtained	Distinction/Division
Class X					
Class XII					
B.Tech./B.E.					

Post Graduate Degree					
PhD					
Any other					

C. Attach list of publications (if any):

D. Working experience (if any):

E. Whether currently employed: Y/N

15. Names and addresses of two referees along with valid contact number and e-mail address.

16. Affix passport-sized photograph with the application.

Declaration

I hereby declare that I have carefully read and understood the instructions and particulars on this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief. In event of the information being found false or incorrect or any ineligibility being found before and after the selection, my candidature is liable to be cancelled. I confirm that I am available for both online/offline interview as informed by the institute.

Date:

Signature

Place:

List of certificates to be attached with this application:

1. B.Tech/B.E final degree certificate.
2. Postgraduate degree M.Tech./M.E. certificate.
3. Passing certificate of GATE/CSIR-UGC NET or any other National Exam Qualification conducted by Central Govt. departments or institutes.
4. PhD certificate (if any)
5. List of publications (if any).
6. Proof of work experience (if any).